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compiler/cpp/src/thrift/generate/t\_xsd\_generator.cc  
compiler/cpp/src/thrift/main.cc  
compiler/cpp/src/thrift/parse/t\_field.h

compiler/cpp/src/thrift/parse/t\_program.h  
compiler/cpp/src/thrift/platform.h  
compiler/cpp/src/thrift/thriftl.ll  
compiler/cpp/src/thrift/thrift.y  
lib/csharp/src/Protocol/TBinaryProtocol.cs  
lib/csharp/src/Protocol/TField.cs  
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lib/csharp/src/Protocol/TMessageType.cs  
lib/csharp/src/Protocol/TProtocol.cs  
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lib/csharp/src/Protocol/TProtocolFactory.cs  
lib/csharp/src/Protocol/TProtocolUtil.cs  
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## 1.3 Brad Appleton - Options 1

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# 1.4 crc 1.0

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# 1.5 CRC-16-CCITT 1.0

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# 1.6 crc32.c N/A

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```
*
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*
* First, the polynomial itself and its table of feedback terms. The
* polynomial is
* X^32+X^26+X^23+X^22+X^16+X^12+X^11+X^10+X^8+X^7+X^5+X^4+X^2+X^1+X^0
*
* Note that we take it "backwards" and put the highest-order term in
* the lowest-order bit. The X^32 term is "implied"; the LSB is the
* X^31 term, etc. The X^0 term (usually shown as "+1") results in
* the MSB being 1
*
* Note that the usual hardware shift register implementation, which
* is what we're using (we're merely optimizing it by doing eight-bit
* chunks at a time) shifts bits into the lowest-order term. In our
* implementation, that means shifting towards the right. Why do we
* do it this way? Because the calculated CRC must be transmitted in
* order from highest-order term to lowest-order term. UARTs transmit
* characters in order from LSB to MSB. By storing the CRC this way
* we hand it to the UART in the order low-byte to high-byte; the UART
* sends each low-bit to high-bit; and the result is transmission bit
* by bit from highest- to lowest-order term without requiring any bit
* shuffling on our part. Reception works similarly
*
* The feedback terms table consists of 256, 32-bit entries. Notes
*
* The table can be generated at runtime if desired; code to do so
* is shown later. It might not be obvious, but the feedback
* terms simply represent the results of eight shift/xor opera
* tions for all combinations of data and CRC register values
*
* The values must be right-shifted by eight bits by the "updcrc
* logic; the shift must be unsigned (bring in zeroes). On some
* hardware you could probably optimize the shift in assembler by
* using byte-swap instructions
* polynomial $edb88320
*/
```

## 1.7 DCE/RPC 1.1 :r0.7

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```
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```

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Release Notes

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Courtney Loomis (Hewlett-Packard Company); Daniel Dardailler (Open Software Foundation)

#### Xlib internationalization

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#### Xt Intrinsic

Douglas Rand (Open Software Foundation), parameterized selections; Paul Asente (Adobe Systems Incorporated), extension event handling; Ajay Vohra (SunSoft), support for multithreading; Sam Chang (Novell), widget caching research; Larry Cable (SunSoft), object allocation and change managed set; Vania Joloboff (Open Software Foundation); Courtney Loomis (Hewlett-Packard Company); Daniel Dardailler (Open Software Foundation); and Ellis Cohen (Open Software Foundation). The following people at Georgia Tech contributed the extensions for disability access:

Keith Edwards, Susan Liebeskind, Beth Mynatt, and Tom Rodriguez.

#### Athena Widget Set

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#### X Logical Font Description

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#### Font Support Enhancements

Nathan Meyers (Hewlett-Packard Company), implementation of matrix enhancement, glyph caching, scalable aliases, sample authorization protocol

#### X Transport Library

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#### X Keyboard Extension

Erik Fortune (Silicon Graphics), design and sample implementation; Jordan Brown (Quarterdeck Office Systems); Will Walker (Digital Equipment Corporation), AccessX portion; Mark Novak (Trace Center), AccessX portion

#### Low-Bandwidth X

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#### X Image Extension

Bob Shelley (AGE Logic), protocol architect, lead implementation architect; Larry Hare (AGE Logic), server implementation; Dean Verheiden (AGE Logic), server implementation; Syd Logan (AGE Logic), xieperf; Gary Rogers (AGE Logic), JPEG code, XIElib documentation; Ben Fahy (AGE Logic), client and server implementation

#### ICCCM

Stuart Marks (SunSoft); Gabe Begeed-Dov (Hewlett-Packard Company); Chan Benson (Hewlett-Packard Company); Jordan Brown (Quarterdeck Office Systems); Larry Cable (SunSoft); Ellis Cohen (Open Software Foundation); Brian Cripe (Hewlett-Packard Company); Susan Dahlberg (Silicon Graphics); Peter Daifuku (Silicon Graphics); Andrew deBlois (Open Software Foundation); Clive Feather (IXI); Christian Jacobi (Xerox PARC); Bill Janssen (Xerox PARC); Vania Joloboff (Open Software Foundation); Phil Karlton (Silicon Graphics); Mark Manasse (Digital Equipment Corporation); Todd Newman (Silicon Graphics); Keith Taylor (Hewlett-Packard Company); Jim VanGilder (Digital Equipment Corporation); Mike Wexler (Kubota Pacific); Michael Yee (Apple Computer)

## ICE

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## SYNC Extension

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## RECORD

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## X Input Extension tests

George Sachs (Hewlett-Packard Company)

PEX Ken Garnett (Shographics); Cheryl Huntington (Sun Microsystems); Karl Schultz (IBM); Jeff Stevenson (Hewlett-Packard Company); Paula Womack (Digital Equipment Corporation)

## Multi-Buffering Extension

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## X server

Milind Pansare (SunSoft), pixmap privates; Peter Daifuku (SGI), layered window support; David Lister (Adobe Systems Incorporated), callback manager; Ken Whaley (Kubota Pacific), thin line pixelization; Joel

McCormack (Digital Equipment Corporation), 64-bit mfb and cfb; Rob Lem-bree (Digital Equipment Corporation), 64-bit mfb and cfb; Davor Matic (MIT), xnest ddx; Nathan Meyers (Hewlett-Packard Company), font support; Jordan Brown (Quarterdeck Office Systems), -config option; Michael Brenner (Apple Computer), macII ddx; Thomas Roell, svga ddx

## Multi-Threaded X Server

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xdm modular loadable greeter

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XFree86 port

Stuart R. Anderson (AT&T Global Information Solutions); Doug Anson; Gertjan Akkerman; Mike Bernson; David Dawes; Marc Evans; Pascal Haible; Matthieu Herrb; Dirk Hohndel; David Holland; Alan Hourihane; Jeffrey Hsu; Glenn Lai; Ted Lemon; Rich Murphey; Hans Nasten; Mark Snitily; Randy Terbush; Jon Tombs; Kees Verstoep; Paul Vixie; Mark Weaver; David Wexelblat; Philip Wheatley; Thomas Wolfram; Orest Zborowski

fonts

Under xc/fonts/, the misc/ directory contains a family of fixed-width fonts from Dale Schumacher, several Kana fonts from Sony Corporation, two Hangul fonts from Daewoo Electronics, two Hebrew fonts from Joseph Friedman, two cursor fonts from Digital Equipment Corporation, and cursor and glyph fonts from Sun Microsystems. The Speedo directory contains outline fonts contributed by Bitstream, Inc. The 75dpi and 100dpi directories contain bitmap fonts contributed by Adobe Systems, Inc., Digital Equipment Corporation, Bitstream, Inc., Bigelow and Holmes, and Sun Microsystems, Inc.

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